**SSESA’s, Science College Congress Nagar, Nagpur**

**Department of Computer Science**

**M. Sc. Semester – II**

**Paper - II (Theory of Computation and Compiler Construction)**

**Assignment List\_2021-22**

**---------------------------------------------------------------------------------------------------------------------** 1. Write the structure of Compiler with suitable Diagram?

2. Explain the following terms.

a) Operator Precedence parsing

b) Top down parsing

3. What are register and address descriptor.

4. Explain Three address code, Quadruples and Triples.

5. Explain Shift reduce parsing with example.

6. Explain the construction of SLR parsing table.

7. Explain the contents and data structures used for symbol tables.

8. Explain the process of code generation from DAG’s.

--------------------------------------------------------------------------------------------------------------